## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

METHOD AND CIRCUIT FOR DYNAMIC READ MARGIN CONTROL OF A MEMORY ARRAY

Application Number : Confirmation Number:

First Named Applicant: Miles Canada

Attorney Docket Number: BUR920040014US1

Art Unit: Examiner:

Search string: (6657912 or 6640266 or 6334167 or 6301183 or 6108266 or 6047248 or 5835421

or 5703823 or 5446695 or 5875135 or 6577548 or 20030026150 or 20030112680 or

20030126172 ).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6657912	2003-12-02	Correale, Jr. et		365	227
				al.			
	2	6640266	2003-10-28	Arcoleo et al.		710	35
	3	6334167	2001-12-25	Gerchman et al.		711	106
	4	6301183	2001-10-09	Bondurant et al.		365	222
	5	6108266	2000-08-22	Weier et al.		365	230.08
	6	6047248	2000-04-04	Georgiou et al.		702	132
	7	5835421	1998-11-10	Pham et al.		365	189.02
	8	5703823	1997-12-30	Douse et al.		365	222
	9	5446695	1995-08-29	Douse et al.		365	222
	10	5875135	1999-02-23	Patwardhan et		365	194
				al.			
	11	6577548	2003-06-10	Barth, Jr. et al.		365	210

## **US Published Applications**

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030026150	2003-02-06	Rimondi			
	2	20030112680	2003-06-19	Nirschl			

3 20030126172	2003-07-03	Chren, Jr.			
ignature					
Examiner Nam	ne	Date			